Developing user propagators for graph-based SMT reasoning

Alec Critten Swansea University

(supervisors: Dr. Monika Seisenberger, Dr. Anton Setzer)

BCTCS 2025 University of Strathclyde, Glasgow, Scotland

15 April 2025

Overview

Background 0000000

- Background
- A tale of two solvers
- Introducing the propagator
- Propagation for graphs
- Conclusion

Project Aims

- Improve the formal verification process of geographic scheme data for railways
- Develop a custom graph theory for the Satisfiability Modulo Theories (SMT) solver Z3
- Integrate graph theory support into a tool-chain for scheme plan verification [1]

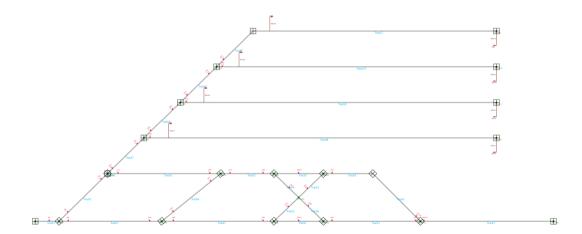
Propagation for graphs

Two key definitions

- Scheme plans are formal geographic representations of railway systems.
- Design rules specify safety-critical constraints which must be verified to hold in the scheme plan.

A scheme plan visualisation

Background 000000



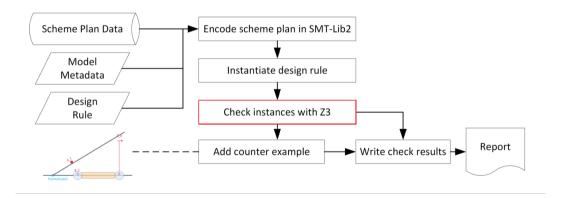
Conclusion

A sample design rule

*The BG-03 design rule*³ states that design placement of balises should avoid points and crossings. Designed spacing shall be constrained by:

- 1. \geq 1.0m between balise and point toes.
- 2. \geq 1.0m between balise and point frog.
- 3. \geq 1.4m lateral separation between a balise on one path and the centre line of the other path.
- 4. No balises between the toe and frog of set of points.

The verification tool-chain [1]



Background

0000

Satisfiability Modulo Theories (SMT):

- generalises propositional satisfiability (SAT) to first-order logic
- uses decision procedures to check satisfiability with respect to background theories

Why SMT?

- Industry demand
- Fully automated theorem proving
- A vision for an expressive language w.r.t. scheme plan verification

Background 000000

On to the modelling!

Approach 1: Modelling with MonoSAT

We started with the MonoSAT SMT solver [2] (with some customisation) because of

- its status as the only known SMT solver which inherently supports graph structures
- its Python interface which allows graph construction in the naïve sense

"Small-Happy" encoding in MonoSAT

```
RBCN11 = small happy.addNode("SimpleNode", 5340, "RBCN11")
25
26
     RBC N111 = small happy.addNode("SimpleNode", 5342, "RBC N111")
27
     RBC N2 = small happy.addNode("SimpleNode", 5969, "RBC N2")
28
     simpleNodes = [RBC S2, RBC S1, EN 3635, RBC N1, RBCN11, RBC N111, RBC N2]
29
30
31
     # points nodes
32
     PN Extra = small happy.addNode("PointsNode", 400, "PN Extra")
34
     points nodes = [PN Extra]
36
37
     # halises
     balise 16 0 = small happy.addNode("Balise", 4348, "balise 16 0")
38
     balise 15 0 = small happy.addNode("Balise", 4386, "balise 15 0")
39
40
     balise 14 0 = small happy.addNode("Balise", 4430, "balise 14 0")
```

```
************
      # Translatina BG-05 into MONOSAT #
107
      **************
      #Designed minimum spacing between adjacent balise groups shall be constrained by:
      #1. ≥ MIN BG SEPARATION between adjacent end balises, one at each end of the two groups.
     MIN BG SEPARATION = 20
      # sort the list of balises
     balises sorted = small_happy.sort_nodes(balise_set)
     print(balises sorted)
      # adjacency approach
     for i in range(len(balises sorted)):
         # check if two end-balises are adjacent
         if (small happy.get adjacent balises(balises sorted[i]) != None):
             if Solve([Not(small happy.distance lt(balises sorted[i], small happy.get adjacent balises(balises sorted[i]), MIN BG SEPARATION))]) == False:
                 print("BG-05 not satisfied")
             Assert(Not(small_happy.distance_lt(balises_sorted[i], small_happy.get_adjacent_balises(balises_sorted[i]), MIN_BG_SEPARATION)))
     print("BG-05 checked")
```

Results from MonoSAT

- Encoded a scheme plan with approx. 50 nodes ("Small-Happy")
- Encoded and instantiated several design rules
- Performed design rule verification in MonoSAT, specifically for balise spacing
- Automated the translation process from scheme plan to graph for MonoSAT

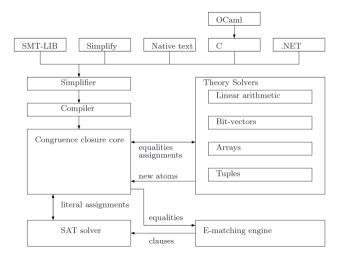
Why pivot away from MonoSAT?

- Bespoke solver not compatible with Z3/general SMT
- Black-box functionality below API
- Difficult to ensure correctness of verification for large scheme plans

Propagation for graphs

Z3 is a deeply complicated system... [3]

Background 0000000



...only well-known by a few experts...





...but there is a solution!

Background 0000000

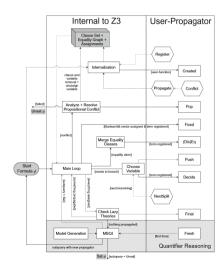
Introducing: user propagators!

What is a user propagator?

A user propagator (Eisenhofer et. al. 2023) [4]:

- is an interface for customised decision procedures
- uses the **EUF** (equality with uninterpreted functions) theory
- operates in an external code-base, exposing callbacks to override Z3 behaviour

User propagators with Z3 architecture [4]





Some callbacks of interest:

- Created instantiate expressions to be eventually assigned to true/false
- Push/Pop save/revert the state of the SAT solver when it branches on a decision, particularly so we can backtrack
- Fixed when the solver decides an expression is true/false
- Final when the solver has assigned all expressions to true/false

The EUF theory

- Always included in SMT
- Everything is **uninterpreted** just symbols
- Congruence closure to determine satisfiability
- Only = comes preloaded with meaning!
- edge is a user-function

The interplay between SAT and theory solvers

Suppose we have $\phi = (x < 3) \land (x > 4)$. Obviously there is no such x, but the solver deduces this way:

 Boolean abstraction: First, the solver abstracts the first-order formula into propositional logic, so

$$\underbrace{(x < 3)}_{A} \land \underbrace{(x > 4)}_{B}$$

Background 0000000

The interplay between SAT and theory solvers, cont.

② Boolean assignment: The SAT solver makes a decision to set $A \to T$ and $B \to T$.

The interplay between SAT and theory solvers, cont.

1 Theory symbols: SMT solver observes x < 3 and x > 4 on a purely symbolic level, then determines they belong to the theory of integer arithmetic (LIA).

The interplay between SAT and theory solvers, cont.

SAT modulo LIA: Solver checks satisfiability of $A \wedge B$ w.r.t. LIA and returns unsat.

Visualising a graph propagation

A simple propagator enforcing the transitive closure relation over a graph $G = \{n_1, n_2, n_3, n_4\}$):



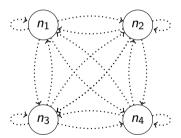
$$(n_2)$$

$$n_3$$

$$(n_4)$$

Visualising a graph propagation, cont.

We instantiate all possible edges as predicates which the solver will definitively assign later:



Background 0000000

Visualising a graph propagation, cont.

If, for example, we are given that $n_1 \rightarrow n_2...$

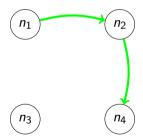






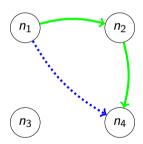
Visualising a graph propagation, cont.

...and $n_2 \rightarrow n_4$...



Visualising a graph propagation, cont.

...then the solver will propagate that $n_1 \rightarrow n_4$:



Propagation for graphs

A comment on transitivity

Constructing the propagator for even a simple relation like TC is not trivial!

Merely programming $(a \rightarrow b) \land (b \rightarrow c) \Rightarrow (a \rightarrow c)$ doesn't work.

The key to transitivity is handling the **permutations** of the order of the edges!

Propagation for graphs

Unlike transitivity, these are easy to model:

- Antireflexivity: for any propagation, check first that the two nodes are distinct
- **Symmetry**: given a propagating edge $a \rightarrow b$:
 - Check if $b \rightarrow a$ is assigned.
 - If not, propagate $b \rightarrow a$ with the single justification $a \rightarrow b$.

.smt2 input (non-sym TC)

```
(declare-const n1 Node)
     (declare-const n2 Node)
3
     (declare-const n3 Node)
4
     (declare-const n4 Node)
5
6
     (assert (edge n1 n2))
     (assert (edge n2 n3))
8
     (assert (edge n3 n4))
9
10
     (check-sat)
```

Custom model generation for graphs...

```
Graph model (16 assignments in total):
                                          (edge n2 n4) -> true
                                          (edge n3 n1) -> false
                                          (edge n3 n2) -> false
(edge n1 n1) -> false
                                          (edge n3 n3) -> false
(edge n1 n2) -> true
                                          (edge n3 n4) -> true
(edge n1 n3) -> true
                                          (edge n4 n1) -> false
(edge n1 n4) -> true
                                          (edge n4 n2) -> false
(edge n2 n1) -> false
                                          (edge n4 n3) -> false
(edge n2 n2) -> false
                                          (edge n4 n4) -> false
(edge n2 n3) -> true
```

...instead of the default Z3 model

```
MODEL FROM Z3:
:: universe for Node:
    Node!val!0 Node!val!1 Node!val!3 Node!val!2
;; definitions for universe elements:
(declare-fun Node!val!0 () Node)
(declare-fun Node!val!1 () Node)
(declare-fun Node!val!3 () Node)
(declare-fun Node!val!2 () Node)
;; cardinality constraint:
(forall ((x Node))
        (or (= x Node!val!0) (= x Node!val!1) (= x Node!val!3) (= x Node!val!2)))
(define-fun n4 () Node
 Node!val!3)
(define-fun n1 () Node
 Node!val!0)
(define-fun n2 () Node
 Node!val!1)
(define-fun n3 () Node
  Node!val!2)
END OF MODEL
```

Propagation for graphs

Logging propagation steps

```
List of unique propagations:
1: (edge n1 n2) = true, derived from input assumption
2: (edge n2 n3) = true, derived from input assumption
3: (edge n1 n3) = true, derived from (ast-vector
  (edge n2 n3)
  (edge n1 n2))
```

We can also model weighted edges as well!

- Similar propagation process to before
- Extend the edge predicate to also take an integer-sort argument

Propagation for graphs

A path predicate (path n1 n2 1) is defined inductively:

- (edge n1 n2 l) \rightarrow (path n1 n2 l)
- (path n1 n2 1) \land (edge n2 n3 1') \rightarrow (path n1 n3 (1 + 1'))

The path propagator: input

```
(declare-const n1 Node)
     (declare-const n2 Node)
3
     (declare-const n3 Node)
     (declare-const n4 Node)
5
     (assert (edge n1 n2 10))
6
     (assert (edge n2 n3 5))
8
     (assert (edge n3 n4 4))
9
     (check-sat)
10
11
     (get-model)
```

The path propagator: output

```
Graph model (9 assignments in total):
(edge n1 n2 10) -> true
(edge n2 n3 5) -> true
(edge n3 n4 4) -> true
(path n1 n2 10) -> true
(path n1 n3 15) -> true
(path n1 n4 19) -> true
<u>(path</u> n2 n3 5) -> true
(path n2 n4 9) -> true
(path n3 n4 4) -> true
```

Naïve shortest path

- Current work
- Naïve approach: generate all paths and take shortest at Final

```
(declare-const n1 Node)
    (declare-const n2 Node)
    (declare-const n3 Node)
4
    (assert (edge n1 n3 10))
6
    (assert (edge n1 n2 1))
    (assert (edge n2 n3 2))
```

```
Graph model (10 assignments in total):
(edge n1 n2 1) -> true
(edge n1 n3 10) -> true
(edge n2 n3 2) -> true
(path n1 n2 1) -> true
(path n1 n3 10) -> true
(path n1 n3 3) -> true
(path n2 n3 2) -> true
(shortest-path n1 n2 1) -> true
(shortest-path n1 n3 3) -> true
(shortest-path n2 n3 2) -> true
```

Propagation for graphs 0000000000000000 Currently working on a graph user propagator for Z3 with verified algorithms. Current and future work planned for this year:

- Optimising path-generation over graphs
- Applying the path propagator to larger, realistic graphs
- Integrating the propagator with our partner's graph tools
- Verifying scheme plans with the railway-oriented propagator

Summary

- Develop **graph theory** for scheme plan verification
- User propagators to customise Z3
- Program decision procedures for graph traversal
- Groundwork for expressing design rule constraints in SMT



Conclusion

0000

References



- Bayless, S., Bayless, N., Hoos, H., Hu, A.: SAT Modulo Monotonic Theories. AAAI Conference on Artificial Intelligence. **29**(1). (2015).
- de Moura, L., Biørner, N.: Z3: An Efficient SMT Solver. TACAS 2008, LNCS 4963. Springer (2008).
 - Bjørner, N., Eisenhofer, C., Kovács, L.: Satisfiability Modulo Custom Theories in Z3. VMCAI 2023. LNCS 13881. Springer (2023).

Thanks for listening!



